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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of** )  
 )  
**YASUI et al.** )  
 )  
**Application Number: To Be Assigned** )  
 )  
**Filed: Concurrently Herewith** )  
 )  
**For: PLANARIZING METHOD OF SEMICONDUCTOR** )  
**WAFER AND APPARATUS THEREOF** )

J1040 U.S. PTO  
09/910904  
07/24/01

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

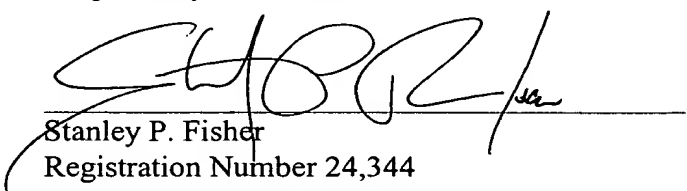
**NOTICE OF PRIORITY  
UNDER 35 U.S.C. § 119  
AND THE INTERNATIONAL CONVENTION**

Sir:

In the matter of the above-captioned application for a United States patent, notice is hereby given that the Applicant claims the priority date of July 27, 2000, the filing date of the corresponding Japanese patent application 2000-231389.

The certified copy of corresponding Japanese patent application 2000-231389 will be submitted as soon as received from Applicant. Acknowledgment of receipt of the certified copy is respectfully requested in due course.

Respectfully submitted,

  
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